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## Oscillator

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### Description (revision A)

In revision A silicon, the oscillator stops running in *Stand-by* (power-down) mode.

### Correction (revision B)

When *Stand-by* mode is requested, the oscillator remains powered up. Also, critical registers (e.g. calibration registers) maintain their values after power-up.

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## Temperature Offset

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### Description (revision A)

In revision A silicon, default values in the *Temperature Gain* and *Temperature Offset* registers need to be adjusted after reset in order to achieve a zero-degree offset.

### Correction (revision B)

Default values adjusted to:

*Temperature Gain* = 0x34E2E7

*Temperature Offset* = 0xF3E7D0

After reset, zero-degree offset is within  $\pm 5^\circ$  C.

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## $\overline{E1}$ & $\overline{E2}$ Pulse Outputs

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### Description (revision A)

When using the  $\overline{E1}$ ,  $\overline{E2}$  pulse output in *Normal* format, with power factor less than one, pulses appear on pulse output  $\overline{E2}$  (which is an indication of negative power). To determine the active energy pulse count use the equation:

$$\text{Active Energy Pulse Count} = \overline{E1} - (2 \times \overline{E2})$$

### Correction (revision B)

In *Normal* format the  $\overline{E1}$  and  $\overline{E2}$  pulse frequencies are calculated from the *Active Power* register. Therefore when:

- 1) Active energy is positive: *Active Energy Pulse Count* = pulse count on  $\overline{E1}$
- 2) Active energy is negative: *Active Energy Pulse Count* = pulse count on  $\overline{E2}$