



SA56

PRELIMINARY DEVICE ERRATA

This document describes the errata information for the SA56 device in the 23 pin SIP package style EX, revision C Beta sample devices. Revision C parts can be identified by the date code marked on the package. Revision C devices are devices with a date code of 0608 or later.

ERRATA NUMBER	DESCRIPTION	IMPACT & WORK AROUND
1 Date codes 0608-0624	TLIM pin functionality: This pin is modified to serve as a flag for any fault occurrence including short-circuit, over current, and over temperature.	Impact: Grounding the TLIM pin disables all fault protection in the SA56 including short circuit, over current, and over temperature. This pin should be left floating at all times unless the user desires to disable all protection mechanisms. This will be rectified to conform to SA56U specifications in the next device revision.
2 0642 only	Disable (pin 18) thresholds: LOGIC LOW INPUT VOLTAGE, V_{IL} Max = 1.8V LOGIC HIGH INPUT VOLTAGE, V_{IH} Min = 3.4V	Impact: The Disable pin is only compatible with CMOS 5V logic. This will be rectified to conform to SA56U specifications in the next device revision.